

## **ABSTRACT**

An apparatus for applying an OFF-state stress to a P-MOS device of one of an array substrate for an LCD device and an LCD panel having attached substrates includes: a power supply unit supplies a source power; a panel loading unit including a plurality of panel jigs on which one of the array substrate and the LCD panel is loaded; a voltage control unit including a plurality of voltage control channels and modulating a voltage of the source power; a frequency control unit including a plurality of frequency control channels and modulating a frequency of the source power; a time setting unit determining a time period of supplying the source power; and a panel selecting unit including a plurality of panel selecting channels and modulating an application of the source power.